IHP GmbH - Leibniz-Institut für innovative Mikroelektronik

IHP is an institute of the Leibniz Association and conducts research and development of silicon-based systems and ultra-high-frequency circuits and technologies including new materials. It develops innovative solutions for application areas such as wireless and broadband communication, security, medical technology, industry 4.0, automotive industry, and aerospace. IHP employs approximately 400 people. It operates a pilot line for technological developments and the preparation of high-speed circuits with 0.13/0.25 µm-SiGe-BiCMOS technologies, located in a 1500 m² cleanroom that meets the highest industrial nanotechnology requirements.

Engineer position (m/f/d) for “Open-source PDK, ADK, EDA Tool, and IC Design IP support on a Cloud system”

Job-ID: 71015/26 |Dept.: Technology | Salary: according TV-L |Working time: 40h/week |Limitation: initially 2 years with option of extension |Entry Date: as soon as possible

City: Frankfurt (Oder); Starting date (earliest): At the earliest possible; Remuneration: TV-L

Tasks

The position:
Join our Research and Prototyping Service group within the Technology department at IHP and contribute to the development and deployment of cloud environments hosting open-source Process Design Kit (PDK) and Electronic Design Automation (EDA) tools and open-source IP examples. Your role will involve:

- Designing, building and maintaining reproducible, secure and scalable IC-design environment on cloud infrastructure
- Installing and testing open-source EDA tools in a cloud environment
- Validating the existing open-source PDK and ADK design flows using test circuits and contributing to the development of training materials
- Conducting user training sessions, providing technical support to users and addressing user inquiries
  You'll be part of an international team of developers who are eager to welcome a new member. We value flat hierarchies and mutual support. Our team benefits from a diversity of perspectives, and we are committed to achieving a balanced gender mix.

Requirements

Your profile:
You hold a Bachelor's or Master’s degree in Computer Sciences, Electrical Engineering, or have relevant practical experience in cloud development or software engineering. You should be comfortable working in a Linux environment and using version control system such as Git. Experience in ASIC design environments with open-source EDA tools is

preferred. Skills in scripting languages such as Python, Bash or similar are required. Basic knowledge of at least one cloud platform. Experience with containerization, automated testing, or CI/CD is an advantage.
You are organized, capable of managing multiple projects in parallel, and maintain a clear overview in dynamic settings. With strong communication skills, you serve as a reliable and engaging contact for partners and collaborators.
You are also a strong team player. We are looking for a team member, who is able to structure his or her own work and to bring a well-organized and systematic way of working into the cooperation with creative minds. You are an ideal match for this position, when you have experimental, analytical and problem-solving skills, very strong communicative skills and the ability to quickly learn how to operate the latest technical equipment including various software. It is necessary that you confidently handle the English language. Knowledge of the German language is welcome.

What we offer

Our Offer:
Do research in a challenging, multinational environment giving you excellent career opportunities. You will have the chance to establish international reputation at the edge of top-notch technologies. An orientation guide will help you to quickly integrate into the institute and to familiarize yourself with the field. It is important to us to support the individual career developments (e.g. conferences, advanced trainings) as well as the personal needs of our employees by offering flexible working hours and the possibility to work off-site. The compatibility of work and family is highly valued. More information about our scientific excellence and the working environment at IHP can be found on our website.
IHP is TOTAL E-QUALITY-certified for equal opportunities for women and men at work and actively pursues the equality of all gender and all groups of people. We promote the professional development of women and strongly encourage them to apply. Disabled applicants, qualified according to the above criteria, will be given preference over other candidates with equivalent relevant qualifications.

Further advantages:
30 days holiday | special annual payment | Company pension scheme (VBL) | Flexible working hours, also part-time (no core working hours) | Possibility to work up to 40 % independent of location according to company agreement | A wide range of further training opportunities in-house or within the framework of business trips | Discounted company ticket with monthly allowance of € 15,75 for various fare zones | Structured induction and actively supported integration into the institute (welcome workshop, intercultural workshop, joint leisure activities)

Application

By internet: https://www.ihp-microelectronics.com/de/karriere/stellenangebote/online-bewerbungsformular?job=70915/26#c977

More information at https://stellenticket.de/207877/FUB/
Offer visible until 25/11/26