

IHP GmbH - Leibniz-Institut für innovative Mikroelektronik



IHP is an institute of the Leibniz Association and conducts research and development of silicon-based systems and ultra-high-frequency circuits and technologies including new materials. It develops innovative solutions for application areas such as wireless and broadband communication, security, medical technology, industry 4.0, automotive industry, and aerospace. IHP employs approximately 410 people. It operates a pilot line for technological developments and the preparation of high-speed circuits with 0.13/0.25 μm -SiGe-BiCMOS technologies, located in a 1500 m² cleanroom that meets the highest industrial nanotechnology requirements.

Research Associate / Postdoc Position (m/f/d) for Brain-Inspired Computing Optimization for Edge AI Applications (in Cottbus)

Job-ID: 30810/26 | Department: System Architecture | Working Time: 40h/week | Salary: E13 TV-L | Limitation: 2 years with option of extension | Entry Date: as soon as possible

City: Cottbus; Starting date (earliest): At the earliest possible; Duration: 2 years;
Remuneration: TV-L

Tasks

We are looking for a motivated researcher to join the System Architectures Department to mainly work on modelling and optimizing brain-inspired architectures for edge AI applications. The development of energy efficient and highly reliable brain-inspired computing architectures requires a holistic strategy assuming all abstraction levels from device, through circuits to architectures. In addition, an application-oriented perspective should be adopted in order to properly model and optimize the use of such architectures assuming different trade-offs between constraints such as power consumption, performance, reliability, etc. When assuming the idea of exploring solutions based on hybrid architectures based on technology heterogeneity, this complexity increases significantly. It is anticipated that the candidate will mainly support projects' activities. Your detailed tasks will include:

- Model and optimization of brain-inspired architectures for edge AI applications assuming technology heterogeneity
- Design and optimization of hardware-aware spiking neural network architectures
- Development of strategies for properly benchmarking different conventional and non-conventional architectures for edge AI applications
- Support the activities in the context of the Neuromorphic Hardware Group (PhD students guidance, project proposals' preparation, scientific papers' preparation, etc.)

Requirements

You hold a PhD's degree in computer science, electrical engineering, or a related field. You have good knowledge and experience in modelling and optimizing brain-inspired computing architecture for edge AI applications assuming technology characteristics as well as trade-offs between requirements and constraints. Additionally, knowledge and passion for conventional and non-conventional AI architectures and heterogeneous integration assuming CMOS and novel devices is favorable.

You will also be a strong team player. We are looking for a team member who can structure their own work and bring a well-organized and systematic approach to working with creative minds. You will be an ideal fit for this position if you have experimental, analytical, and problem-solving skills, very strong communication skills, and the ability to quickly learn how to use the latest technical equipment, including various software. You must be fluent in English.

German language skills are welcome. Further development of German language skills is expected and strongly encouraged, e.g. through in-house language courses and intensive courses.

What we offer

Conduct research in a challenging, multinational environment giving you excellent career opportunities. You will have the chance to establish international reputation at the edge of top-notch technologies. It is important to us to support the individual career developments (e.g. conferences, advanced trainings) as well as the personal needs of our employees by offering flexible working hours and the possibility to work off-site. The compatibility of work and family is highly valued. More information about our scientific excellence and the working environment at IHP can be found on our website.

IHP is TOTAL E-QUALITY-certified for equal opportunities for women and men at work and actively pursues the equality of all gender and all groups of people. We promote the professional development of women and strongly encourage them to apply. Disabled applicants, qualified according to the above criteria, will be given preference over other candidates with equivalent relevant qualifications.

Further advantages:

30 days holiday | special annual payment | Company pension scheme (VBL) | Flexible working hours, also part-time (no core working hours) | Possibility to work up to 40 % independent of location according to company agreement | A wide range of further training opportunities in-house or within the framework of business trips | Discounted company ticket with monthly allowance of € 15,75 for various fare zones | Good transport connections, free parking at the institute | Structured induction and actively supported integration into the institute (welcome workshop, intercultural workshop, joint leisure activities)

Application

Contact person: IHP

By internet: <https://www.ihp-microelectronics.com/career/vacancies/online-application-form?job=30810/26>

More information at <https://stellenticket.de/206333/HTWB/>
Offer visible until 03/09/26

