Jane Street Europe Limited

Jane Street — We are a global liquidity provider and trading firm that uses sophisticated quantitative analysis and a deep understanding of market mechanics to help keep prices consistent and reliable.

FPGA Engineer - 2026 Summer Internship

City: London; Starting date (earliest): At the earliest possible; Remuneration: Competitive

Working field

About the position

In this internship, you'll learn how we use tools to make programming faster, more pleasant, and more reliable. We apply these same principles to our FPGA Engineering work, and we're looking for people who are interested in using programming language technology to improve the process of designing, testing, and validating hardware designs. We use Hardcaml, an OCaml library for succinctly describing hardware in RTL. Hardcaml is tightly integrated into our development environment, so you'll also gain lots of exposure to the libraries and tools that are foundational to our internal systems. No previous knowledge of Hardcaml is required.

During the program, you'll dive deep on one project, mentored closely by the full-time employees who helped design it. Some intern projects consider big-picture questions that we're still trying to figure out, while others involve building something new. Your mentors will help you gain a better understanding of the wide range of problems we solve every day. We expect interns to build FPGA applications from concept to a working design; your projects will predominantly involve OCaml & Hardcaml, for both RTL design and testing/integration.

If you'd like to learn more, you can read about our interview process, meet some of our newest hires, or check out our OCaml All The Way Down talk and Programmable Hardware podcast episode.

Requirements

About you

We don't expect you to have a background in finance, OCaml, functional programming, or any other specific field—we're looking for smart people who enjoy solving interesting problems. We're more interested in how you think and learn than what you currently know. You should be:

- Comfortable with a software programming language
- Experienced with a Hardware Description (or Construction) language (VHDL,

Verilog, Chisel, Pymtl, or other), for both writing and testing hardware designs
- Experienced working with FPGA vendor tools - Vivado or Quartus
- Experienced with building a working FPGA project through an academic, professional, or personal project
- Interested in learning how to use FPGAs in the context of networking
- Fluent in English

What we offer

About the program

Our goal is to give you a real sense of what it's like to work at Jane Street full time. Over the course of your internship, you will explore ways to approach and solve exciting problems within your field of interest through fun and challenging classes, interactive sessions, and group discussions—and then you will have the chance to put those lessons to practical use.

As an intern, you are paired with full-time employees who act as mentors, collaborating with you on real-world projects we actually need done. When you're not working on your project, you will have plenty of time to use our office amenities, physical and virtual educational resources, attend guest speakers and social events, and engage with the parts of our work that excite you the most.

If you've never thought about a career in finance, you're in good company. Many of us were in the same position before working here. If you have a curious mind, a collaborative spirit, and a passion for solving interesting problems, we have a feeling you'll fit right in.

Learn more about Jane Street's internship program here.

Application

To apply, please visit our website: https://www.janestreet.com/join-jane-street/open-roles/?type=students-and-new-grads&location=london

More information at https://stellenticket.de/198975/FUB/
Offer visible until 22/12/25

Stellenticket