About you

We don't expect you to have a background in finance, OCaml, functional programming, or any other specific field—we're looking for smart people who enjoy solving interesting problems. We're more interested in how you think and learn than what you currently know. You should be:

- Comfortable with a software programming language
- Experienced with a Hardware Description (or Construction) language (VHDL, Verilog, Chisel, Pymtl, or other), for both writing and testing hardware designs
- Experienced working with FPGA or ASIC vendor tools - Vivado or Quartus for FPGAs, Genus or Innovus for ASICs
- Experienced with building a working hardware project (either FPGA or ASIC) through an academic, professional, or personal project
- Interested in learning how to use FPGAs or ASICs in the context of networking
- Fluent in English

Application

Please apply directly through our website. Thank you!

By internet: https://www.janestreet.com/join-jane-street/open-roles/?type=students-and-new-grads&location=london

More information at https://stellenticket.de/207627/FUB/
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