

IHP GmbH - Leibniz-Institut für innovative Mikroelektronik



Das IHP ist ein Institut der Leibniz-Gemeinschaft und betreibt Forschung und Entwicklung zu siliziumbasierten Systemen, Höchstfrequenz-Schaltungen und -Technologien einschließlich neuer Materialien. Es erarbeitet innovative Lösungen für Anwendungsbereiche wie die drahtlose und Breitbandkommunikation, Sicherheit, Medizintechnik, Industrie 4.0, Mobilität und Raumfahrt. Das IHP beschäftigt ca. 330 Mitarbeiterinnen und Mitarbeiter. Es verfügt über eine Pilotlinie für technologische Entwicklungen und die Präparation von Hochgeschwindigkeits-Schaltkreisen mit 0,13/0,25 µm-BiCMOS-Technologien, die sich in einem 1000 m² großen Reinraum der Klasse 1 befindetet.

Research Engineer (m/f/d) for DevOps and Design Infrastructure

Job-ID: 3046/26 | Department: System Architecture | Working Time: 40h/week | Salary: E13 TV-L | Limitation: 2 years with option of extension | Entry Date: as soon as possible

City: Frankfurt (Oder); Starting date (earliest): At the earliest possible; Remuneration: TV-L

Tasks

The position:

We are looking for a technically skilled engineer to join the System Architectures Department as our in-house interface to the central IT infrastructure. You will be responsible for setting up, maintaining, and operating the full technical stack our research and development work depends on — from bare-metal servers and EDA tool environments to design flows and compute cluster management. The goal is to ensure our teams get what they need, when they need it.

Beyond operations, you will help us turn our hardware development infrastructure into a living data source. By extracting, structuring, and processing data from our design flows and tools, you will enable real-time insight into our development processes — an area with genuine research potential and the possibility of contributing to publications. As our in-house DevOps engineer, you will build and operate the technical foundation our hardware development teams rely on:

- Setup and administration of servers, operating systems, and virtualization environments
- EDA tool and HW development flow management, including license administration
- Repository management and CI/CD pipelines for hardware and software IP
- AI compute cluster management, including resource allocation and user access

Beyond infrastructure operations, you will help us make our design data actionable. This includes extracting, structuring, and processing data from our development flows and tools so that we can monitor, analyze, and act on it in real time:

- Data governance, modeling, and IP inventory management
- Database setup and maintenance for design and flow data

- Metadata enrichment and data pipeline development
- Evaluation of tools and frameworks for data management and visualization
- Development of internal dashboards and interfaces for live monitoring of infrastructure and design flows

Requirements

Your qualifications:

You hold a Master's degree in Computer Science, IT Engineering, or a related field. You have solid experience in Linux system administration and DevOps practices, including version control, CI/CD tooling, and containerization. A structured approach to data management and documentation is expected. Familiarity with EDA or hardware development environments is a strong plus. Experience with databases, data pipelines, or visualization tools is beneficial. You enjoy working closely with research and engineering teams and take ownership of the infrastructure they rely on.

You will also be a strong team player. We are looking for a team member who can structure their own work and bring a well-organized and systematic approach to working with creative minds. You will be an ideal fit for this position if you have experimental, analytical, and problem-solving skills, very strong communication skills, and the ability to quickly learn how to use the latest technical equipment, including various software. You must be fluent in English.

German language skills are welcome. Further development of German language skills is expected and strongly encouraged, e.g. through in-house language courses and intensive courses.

What we offer

Our Offer:

Conduct research in a challenging, multinational environment giving you excellent career opportunities. You will have the chance to establish international reputation at the edge of top-notch technologies. It is important to us to support the individual career developments (e.g. conferences, advanced trainings) as well as the personal needs of our employees by offering flexible working hours and the possibility to work off-site. The compatibility of work and family is highly valued. More information about our scientific excellence and the working environment at IHP can be found on our website.

IHP is TOTAL E-QUALITY-certified for equal opportunities for women and men at work and actively pursues the equality of all gender and all groups of people. We promote the professional development of women and strongly encourage them to apply. Disabled applicants, qualified according to the above criteria, will be given preference over other candidates with equivalent relevant qualifications.

Further advantages:

30 days holiday | special annual payment | Company pension scheme (VBL) | Flexible working hours, also part-time (no core working hours) | Possibility to work up to 40 % independent of location according to company agreement | A wide range of further training opportunities in-house or within the framework of business trips | Discounted company ticket with monthly allowance of € 15 for various fare zones | Good transport connections, free parking at the institute | Structured induction and actively supported integration into the institute (welcome workshop, intercultural workshop, joint leisure activities)

Application

Contact person: Markus Ulbricht

By internet: <https://www.ihp-microelectronics.com/career/vacancies/online-application-form?job=3046/26>

More information at <https://stellenticket.de/203790/FUB/>
Offer visible until 29/05/26

